

ABSTRACT OF THE DISCLOSURE

A semiconductor device having a protection circuit comprising an NPN type bipolar transistor having a collector and an emitter connected between an external connection terminal of the semiconductor device to be protected and a reference terminal, a PMOS transistor having a drain and source connected between the base and the collector of the NPN type bipolar transistor and configured to supply a base current to the base of the NPN type bipolar transistor, and a control circuit configured to supply the control signal to the gate of the PMOS transistor in response to a voltage emerging on the external connection terminal.